DDDDDDDDDDDD RRRRI	RRRRRRR RRRRRRR RRRRRRRR		VVV VVV	VVV VVV		RRRRR	RRRRRRR RRRRRRR RRRRRRR	
DDD DDD RRR	RRR	iii	VVV	VVV	EEE	RRR		RRR
DDD DDD RRR	RRR	111	VVV	VVV	EEE	RRR		RRR
DDD DDD RRR	RRR	111	VVV	VVV	EEE	RRR		RRR
DDD DDD RRR	RRR	iii	VVV	VVV	ĒĒĒ	RRR		RRR
DDD DDD RRR	RRR	III	VVV	VVV	EEE	RRR		RRR
	RRRRRRRR	III	VVV	VVV	EEEEEEEEEE		RRRRRRR	
	RRRRRRRR	111	VVV	VVV	EEEEEEEEEEE		RRRRRRR	
DDD DDD RRRRI	RRRRRRRR	111	VVV	VVV	EEEEEEEEEEE	RRR	RRRRRRR	
DDD DDD RRR	RRR	111	VVV	VVV	EEE	RRR	RRR	
DDD DDD RRR	RRR	iii	VVV	VVV	ĒĒĒ	RRR	RRR	
DDD DDD RRR	RRR	111	VVV	VVV	EEE	RRR	RRR	
DDD DDD RRR	RRR	III	VVV	VVV	EEE	RRR	RRR	
DDD DDD RRR	RRR	!!!	VVV	VVV	EEE	RRR	RRR	000
DDDDDDDDDDDD RRR	RRR	111111111	V/		EEEEEEEEEEEEEE	RRR		RRR
DDDDDDDDDDD RRR	RRR	111111111	V		EEEEEEEEEEEE	RRR		RRR

\_1

PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA	TTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTT	AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA	BBBBBBBB BBBBBBBB BB		\$	
		\$					

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E 7 16-SEP-1984 01:07:58 VAX/VMS Macro V04-00 PATABLES Table of contents Page 0 DEFINITIONS
DRIVER PROLOGUE TABLE
DRIVER DISPATCH TABLE
FUNCTION DECISION TABLE 63 91 130 151

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.TITLE VO4-000

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FACILITY:

VAX/VMS EXECUTIVE, I/O DRIVERS

ABSTRACT: THIS MODULE CONTAINS THE DRIVER PROLOGUE TABLE, DRIVER DISPATCH TABLE, AND FUNCTION DECISION TABLE.

AUTHOR: N. KRONENBERG, JUNE 1981

MODIFIED BY:

NPK3057 N. Kronenberg Change retry count from 10. to 50. V03-006 NPK3057 23-Jul-1984

V03-005 NPK3029 NPK3029 N. Kronenberg Change retry count from 3 to 10. 22-Jul-1983

Add \$DEVDEF and \$SSDEF. V03-004 KTA3046 03-Apr-1983

8-JUN-1982 V03-003 R0W0099 Ralph O. Weber Add error log buffer size and register dump routine entries in the DDTAB macro. This change will be in a new driver image shipped in V3.1.

V03-002 NPK2019 N. Kronenberg 6-Apr-1982 Make start I/O routine return ill function code instead of bugcheck.

222222222222333333333333

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16-SEP-1984 01:07:58 VAX/VMS Macro V04-00 Page 5-SEP-1984 00:17:04 [DRIVER.SRCJPATABLES.MAR:1

18-Mar-1982

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V03-001 NPK2016 Fixed .TITLE

N. Kronenberg

58 59 60 61 :--

DEFINITIONS

.SBTTL DEFINITIONS

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System definitions (LIB.MLB): SCRBDEF SDCDEF SDDBDEF SDEVDEF SDPTDEF **SDYNDEF** SIPLDEF SPDTDEF SUCBDEF \$SSDEF SVECDEF

Channel Request Block offsets
Device type codes
Device Data Block offsets
Device definitions
Driver Prologue Table offsets
Dynamic block types
IPL definitions
Port Descriptor Table offsets
Unit Control Block offsets
System service success codes
CRB transfer vector blk offsets

PADRIVER definitions (PALIB.MLB):

**SPAPDTDEF** SPAREGDEF **SPAUCBDEF** 

;CI extension to PDT ;CI port register definitions ;CI extension to UCB

(3)

DPT\_STORE

DPT\_STORE

DPT\_STORE

DPT\_STORE

CRB,CRB\$L\_INTD+VEC\$L\_INITIAL,-D,PA\$CTLINIT ;Contro

CRB, CRB\$L\_INTD+VEC\$L\_UNITINIT,-D, PA\$UNITINIT ;Unit in

CRB, CRB\$L TOUTROUT, -D, CNF\$TIMER END

Controller init addr

:Periodic wake up routine

Unit init addr

: If ever get here, then : return error to caller : QIO

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PATABLES V04-000	FUNCTION (	DECISION TABLE	K 7 16-SEP-1984 01:07:58 5-SEP-1984 00:17:04	VAX/VMS Macro V04-00 [DRIVER.SRC]PATABLES.MAR; 1	Page 6
	0045 0045 0045 0045 0045 0040 0040 0055 0055	151 .SBTTL 152 153 PA\$FUNCTABLE: 154 155 FUNCTAB 156 157 158 FUNCTAB 159 160 161 162 163 164 165 .END		;Valid functions: ;None at present ;Buffered functions:	

PL

	PATABLES Symbol table				L 7	16-SEP-1984 01:07 5-SEP-1984 00:17	:58 VAX/VMS Macro VO4-00 :04 [DRIVER.SRC]PATABLES.MAR;1	Page	7,	
	\$55	= 00000020	R	02	PA_PIC				(5)	
	\$\$\$CURSIZ \$\$\$NEWSIZ	= 00000020 = 00000104 = 00000100 = 00000002 = 00000004			PA_PIC PA_PMC PA_PPR PA_PPR PA_PSB PA_PSR PDT\$B_DQIMAP PDT\$B_HSHUT_DG PDT\$B_MAX_PORT PDT\$B_NXT_PORT PDT\$B_PO_EBSTS PDT\$B_PI_LBSTS PDT\$B_PI_LBSTS PDT\$B_PORT NUM PDT\$B_PORT NUM PDT\$B_REQIDPS PDT\$C_PAREGBASE P		0000004 00000940			
	SSOP ATS_CI CNFSTIMER	= 00000002	x	02	PA_PS PA_PSR		00000900			
	CRB\$L_INTD CRB\$L_TOUTROUT DC\$_BUS DDB\$L_DDT	= 00000024 = 0000001C			PDTSB_DQIMAP		00000154 00000180			
	DDBSL_DDT	= 00000080 = 0000000C			PDTSB_MAX_PORT		0000017C 0000017E			
1	DEVSM_ELG DEVSM_IDV	= 00400000 = 04000000			PDTSB_PLOGMAP		00000181 00000134			
	DEVSM_ODV DEVSM_SHR	= 08000000 = 00010000			PDTSB_PORTMAP PDTSB_PORT_NUM		00000114 0000017D			
	DEV\$M_AVL DEV\$M_ELG DEV\$M_IDV DEV\$M_ODV DEV\$M_ODV DEV\$M_SHR DPT\$C_LENGTH DPT\$C_VERSION DPT\$INITAB	= 00000024 = 0000001C = 00000080 = 0000000C = 00040000 = 0400000 = 04000000 = 08000000 = 00000038 = 00000004 = 00000008 00000053 00000000		02	PDTSB_REGIDPS PDTSC_LENGTH		0000017F 000000E4			
	DPT\$M_SCS	= 00000004 = 00000008	•	02	PDTSC PAREGEND		00000110 000001E0			
	DPTSREINITAB DPTSTAB	00000053	R	02	PDT\$L_CNF PDT\$L_CQO		000000E4 000000F0			
	DYNSC_CRB DYNSC_DDB DYNSC_DPT	= 00000005 = 00000006 = 0000001E = 00000010			PDTSL_DFQ PDTSL_DFQHDR		00000F4 000000FC 00000208			
	DYNSC_DDB DYNSC_DPT DYNSC_UCB ELOGSK_BYTES ELOGSREGDUMP	******	x	03	PDT\$L_DGHDRSZ PDT\$L_DGNETHD		00000190 00000194			
	FATAL QIO FUNCTAB LEN IOC\$MNTVER	00000038	RX	03 03 03	PDTSL_DQELOGOUT		000002E0 0000022C			
	IOC\$REQCOM	******	X	03 03 03	PDT\$L_LBDG PDT\$L_MFQ		00000184 00000100			
	IOC\$RETURN IPL\$_SCS	= 00000008	X	03	PDT\$L_MGELOGOUT		0000020C 00000320			
	MASKA MASKL PASCTLINIT	= 00000008 = 00000000 = 00000000	×	02	PDTSL_PFAR		00000104 00000108 00000068			
	PASDDT PASEND	00000000	X	03 02	PDT\$L_POLLERDUE		0000018C 00000188			
	PASFUNCTABLE PASINT PASUNITINIT	00000045	R X	02 03 02 03 02 02	PDTSL_PPR PDTSL_PS		0000010C 000000EC			
	PA_CNF PA_CQO	00000000	^	02	PDT\$L_SPTBASE		00000924 00000940 00000904 00000904 00000918 00000154 0000017C 00000180 00000181 00000184 0000017P 0000017P 0000017P 00000164 00000100 00000160 00000160 00000190 00000194 00000190 00000194 00000190 00000190 00000190 00000190 00000190 00000188 00000100 00000188 0000010C 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188 00000188			
	PA_CQ2	0000090C 00000910			PDT\$L_VBDT PDT\$L_VPQB		0000021C 00000218			
	PA_CNF PA_CQ0 PA_CQ1 PA_CQ2 PA_CQ3 PA_DFQ PA_MADR PA_MADR	00000914			PDT\$Q_COMQ3 PDT\$Q_COMQBASE		000001F0 000001F8 000001E0			
	PA_MDATR PA_MFQ	00000018 00000920			PDTSQ_COMQH PDTSQ_COMQL		000001E8 000001E0			
	PA_MFQ PA_MTC PA_MTEC PA_PDC PA_PEC	00000934			PDTSQ_FORMPB PDTSQ_MEREEQ		000001B0 00000174 000001B8			
	PA_PESR	00000000 00000908 00000910 00000914 00000928 00000014 00000920 00000930 00000930 00000930 00000930			PDTSL MQELOGOUT PDTSL MTC PDTSL PFAR PDTSL PMC PDTSL POLLERDUE PDTSL PPR PDTSL PSR PDTSL PSR PDTSL SPTBASE PDTSL SPTBASE PDTSL SPTLEN PDTSL VPQB PDTSL VPQB PDTSQ COMQ2 PDTSQ COMQ3 PDTSQ COMQ4 PDTSQ COMQ4 PDTSQ COMQ4 PDTSQ TCMQ6 PDTSQ TCMQ6 PDTSQ TCMQ6 PDTSQ TCMQ6 PDTSQ TCMQ6 PDTSQ TCMQ6 PDTSQ TCMC7 PDTSQ TCMC8 PDTSQ		000001E0 000001E8 000001E0 000001D0 00000174 000001D8 00000200 0000019C			
1	PA_PFAR	00000938			PDTSW_BDTLEN		00000220			

```
M 7
             16-SEP-1984 01:07:58 VAX/VMS Macro V04-00 
5-SEP-1984 00:17:04 [DRIVER.SRC]PATABLES.MAR;1
                                                                                                              (5)
```

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## ! Psect synopsis

PSECT name	Allocation	PSECT No.	Attributes			
*ABS . \$ABS\$ \$\$\$105_PROLOGUE \$\$\$115_DRIVER	00000000 ( 0.) 00000944 ( 2372.) 00000060 ( 109.) 00000055 ( 85.)	00 ( 0.) 01 ( 1.) 02 ( 2.) 03 ( 3.)	NOPIC USR C	ON ABS L	CL NOSHR NOEXE CL NOSHR EXE CL NOSHR EXE CL NOSHR EXE	NORD NOWRT NOVEC BYTE RD WRT NOVEC BYTE RD WRT NOVEC BYTE RD WRT NOVEC LONG

## Performance indicators

Phase	Page faults	CPU Time	Elapsed Time
Initialization Command processing Pass 1	35	00:00:00.07 00:00:00.50 00:00:11.75 00:00:01.67	00:00:00.34 00:00:04.46 00:00:38.66 00:00:04.79
Command processing	133 418	00:00:00.50	00:00:04.46
Pass 1	418	00:00:11.75	00:00:38.66
Symbol table sort	0	00:00:01.67	00:00:04.79

00000210 00000110 00000214 00000112 00000198 00000019A = 00000001 = 000000040 = 0000005E = 00000080 = 00000081 = 00000001 = 000000001 = 00000001 = 00000001

= 000001B4 = 000000B4 = 00000040 000000F0 00000038 = 00000090 000000A0 800000D8 00000DE 000000E4 000000EA 000000F8 000000B8 000000D4 000000F4 000000F6 = 0000000C = 00000018

PATABLES Symbol table

PDT\$W\_DQELEN
PDT\$W\_LPORT\_STS
PDT\$W\_MQELEN
PDT\$W\_PBCOUNT
PDT\$W\_STDGDYN
PDT\$W\_STDGUSED

PDTSW\_STDGDYN
PDTSW\_STDGUSED
SIZ...
SS\$\_ILLIOFUNC
UCB\$B\_DEVCLASS
UCB\$B\_DIPL
UCB\$B\_ERTCNT
UCB\$B\_ERTMAX
UCB\$B\_ERTMAX
UCB\$B\_LMERTCNT
UCB\$B\_LMERTCNT
UCB\$B\_LMEST
UCB\$C\_PASIZE
UCB\$K\_ERRDGBYTS
UCB\$L\_ERRDGBYTS
UCB\$L\_CICMD
UCB\$L\_DEVCHAR
UCB\$L\_DEVCHA

P

The working set limit was 1650 pages.
91602 bytes (179 pages) of virtual memory were used to buffer the intermediate code.
There were 90 pages of symbol table space allocated to hold 1619 non-local and 0 local symbols.
165 source lines were read in Pass 1, producing 15 object records in Pass 2.
31 pages of virtual memory were used to define 27 macros.

! Macro library statistics !

Macro Library name

\$255\$DUA28:[DRIVER.OBJ]PALIB.MLB;1

\$255\$DUA28:[SYS.OBJ]LIB.MLB;1

\$255\$DUA28:[SYSLIB]STARLET.MLB;2

TOTALS (all libraries)

Macros defined

14

8
25

1981 GETS were required to define 25 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:PATABLES/OBJ=OBJ\$:PATABLES MSRC\$:PATABLES/UPDATE=(ENH\$:PATABLES)+EXECML\$/LIB+LIB\$:PALIB.MLB/LIB

0115 AH-BT13A-SE VAX/VMS V4.0

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